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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No.	6,535,780
Confirmation No.	7801
Issue Date	
Inventorship	
Applicant	
Group Art Unit	-
Examiner	
Attorney's Docket No	LPAG0068
Title: HIGH SPEED PROGRAMMER SYSTEM	

REQUEST FOR CERTIFICATE OF CORRECTION

UNDER 37 CFR 1.323

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To: Honorable Commissioner of Patents

P.O. Box 1450

Alexandria, VA 22313-1450

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Lance R. Sadler (Tel. 509-755-7262; Fax 509-755-7252)

Sadler, Breen, Morasch & Colby, p.s. 422 W. Riverside Avenue, Suite 424

Spokane, WA 99201

Customer No. 75671

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Sir:

From:

We respectfully request that a Certificate of Correction of Applicant's mistake be issued in accord with enclosed form PTO-1050. The errors listed in form PTO-1050 are believed to be of a clerical or typographical nature, or of minor character, which was not the fault of the Patent and Trademark Office. Further, the proposed corrections are not believed to constitute new matter or require reexamination. In addition, we are aware of the required fee set forth in 37 CFR 1.20(a) for providing a correction of applicant's mistake. Accordingly, the

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Commissioner is hereby authorized to charge any fee or credit any overpayments to Deposit Account No. 50-4143 of Sadler, Breen, Morasch & Colby.

We respectfully submit that error is present in the printed patent and that correction thereof in accord with enclosed form PTO-1050 is required to prevent any misunderstanding.

Respectfully Submitted,

Lance R. Sadler

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Date: 8/26/2008

By: ___/Lance R. Sadler/

Reg. No. 38,605 Attorney for Applicant Sadler, Breen, Morasch & Colby, PS 422 W. Riverside Avenue, Suite 424

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SBMC, p.s.

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UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO.

: 6,535,780 B1

Page 1 of 1

DATED

APPLICATION NO.: 09/471675 : March 18, 2003

INVENTOR(S)

: George Leland Anderson et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 4, line 23, before "converter" delete "digitalto-analog" and insert -- digital-to-analog --, therefor.

In column 4, line 26, before "the processor" delete "amplifer" and insert -- amplifier --, therefor.

In column 9, line 8, before "have" insert -- may --.

In column 11, line 67, before "circuit" delete "sources" and insert -- source --, therefor.

In column 11, line 46, before "buffer" delete "the" and insert -- a --, therefor.

In column 12, line 45, after "the" delete "amplifer" and insert -- amplifier --, therefor.

Signed and Sealed this

Sixth Day of April, 2010

David J. Kappos Director of the United States Patent and Trademark Office

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO.

: 6,535,780

APPLICATION NO.: 09/471,675

ISSUE DATE

: March 18, 2003

INVENTOR(S)

: George Leland Anderson et al.

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In column 12, line 45, after "the" delete "amplifer" and insert - - amplifier - -, therefor.

MAILING ADDRESS OF SENDER (Please do not use customer number below):

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This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.